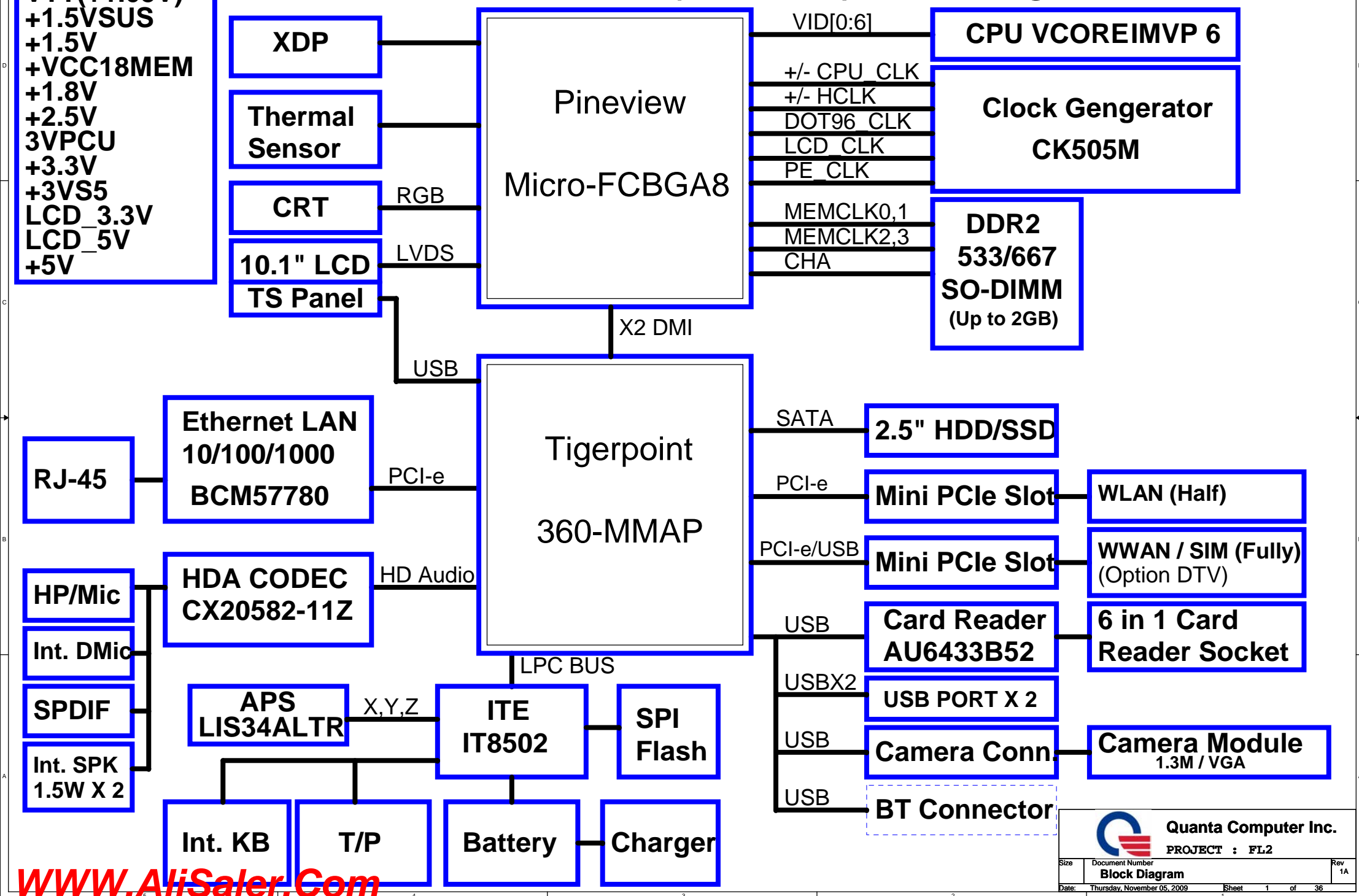

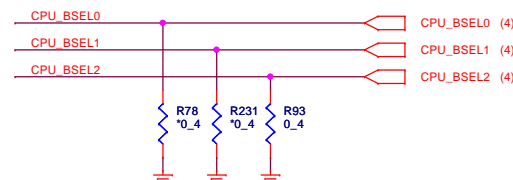


Lenovo Caucasus 2 (Pine Trail) Block Diagram



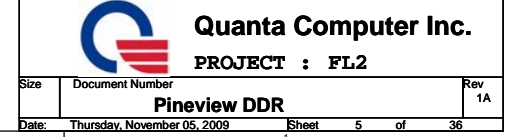
[illegible][illegible]

 Quanta Computer Inc. PROJECT : FL2	
Size	Document Number POWER MANAGER
Date: Thursday, November 05, 2009	Sheet 2 of 36 Rev 1A



WWW.AliSaler.Com

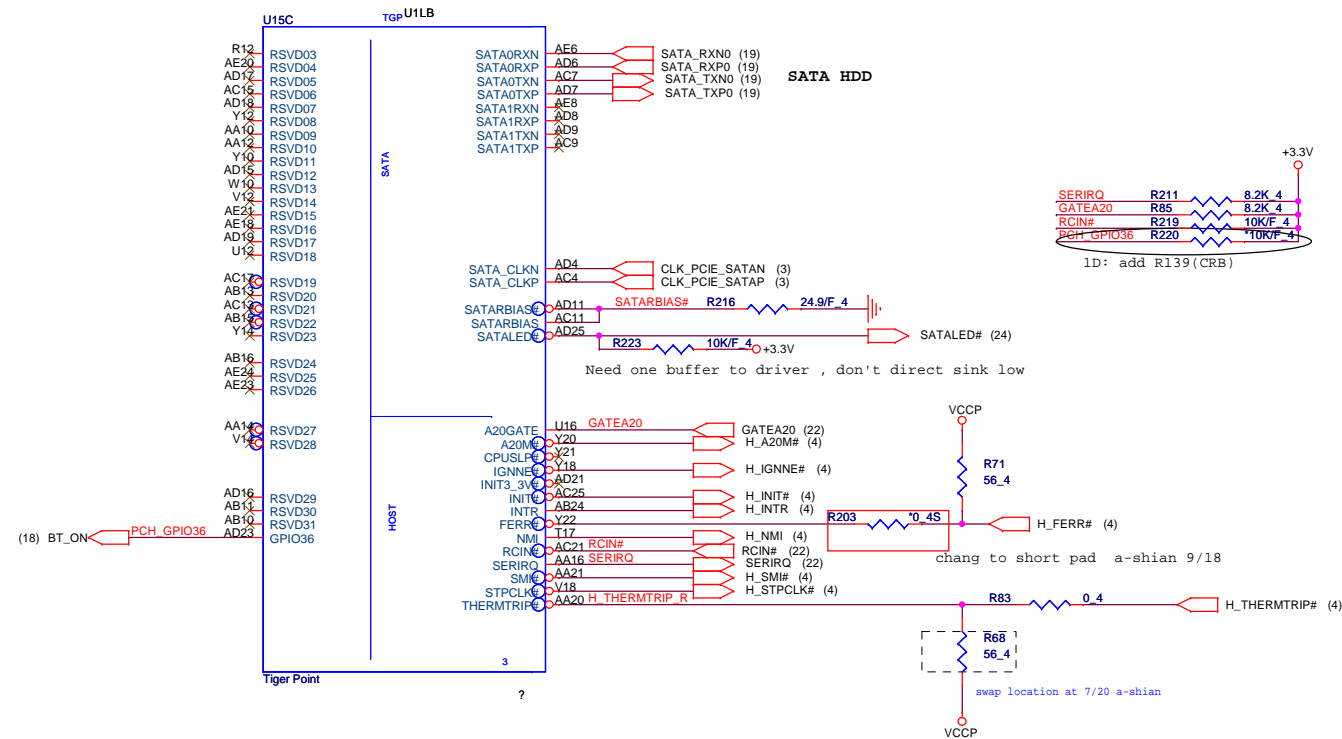








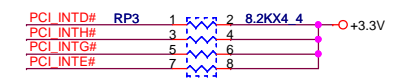
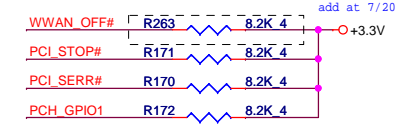
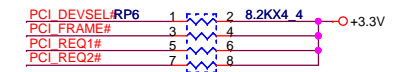
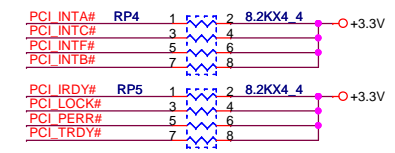
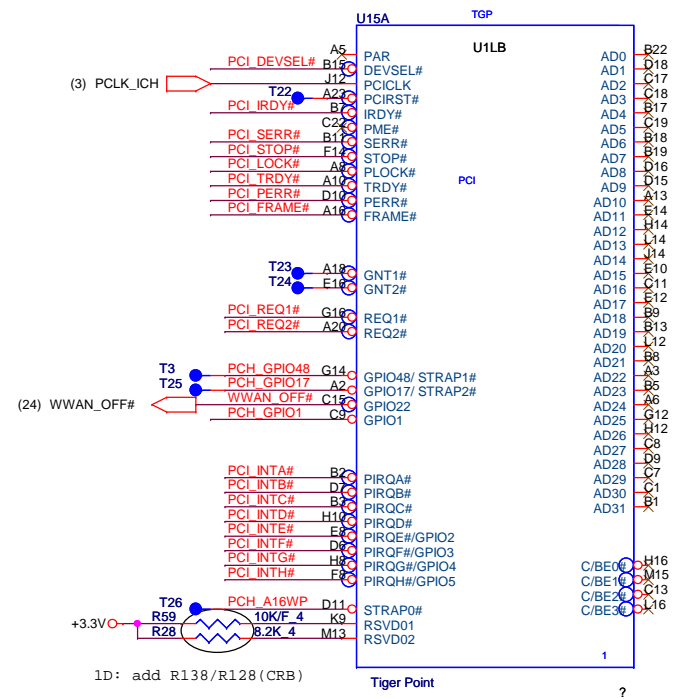




Quanta Computer Inc.
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Tiger Point Sata/Host




IRQ	Description
PIRQA	USB UHCI Controller #1, #4
PIRQB	AC'97 Codec; option for SMBUS
PIRQC	USB UH Controller #3; SATA/IDE Native Mode
PIRQD	USB UHCI Controller #2
PIRQE	Internal LAN; Option for SCI, TCO, HPET#0,1,2
PIRQF	Option for SCI, TCO, HPET#0,1,2
PIRQG	Option for SCI, TCO, HPET#0,1,2
PIRQH	USB EHCI Controller; Option for SCI, TCO, HPET#0,1,2
PCI_GNT#2	Internal PU Should not be PD

ICH Boot BIOS select

PCH_GPIO17 (INT PU)	PCH_GPIO48 (INT PU)	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC (Default)

A16 SWAP Override strap

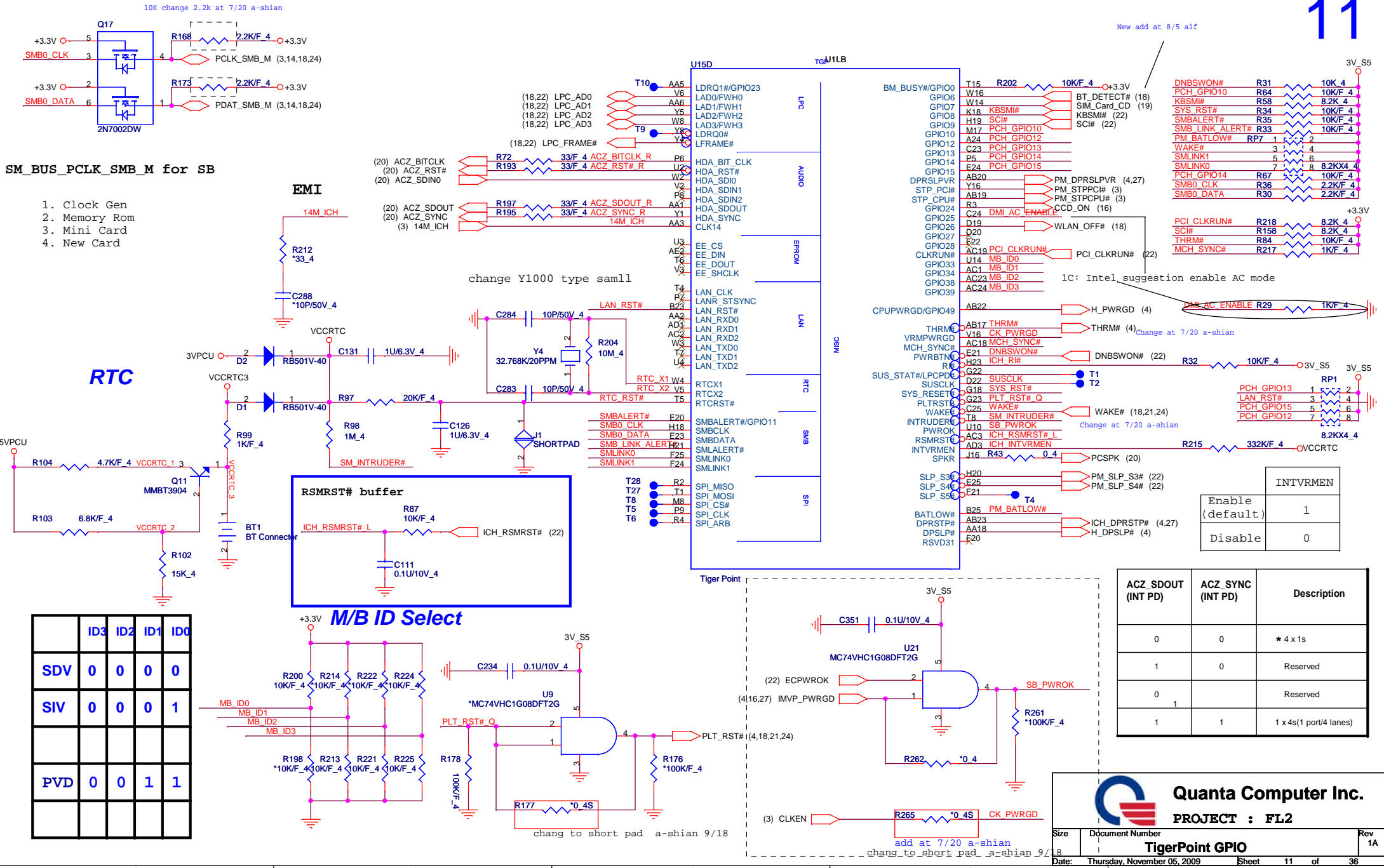
PCH_A16WP (INT PU)	Low = A16 swap override enabled High = Default
-----------------------	---



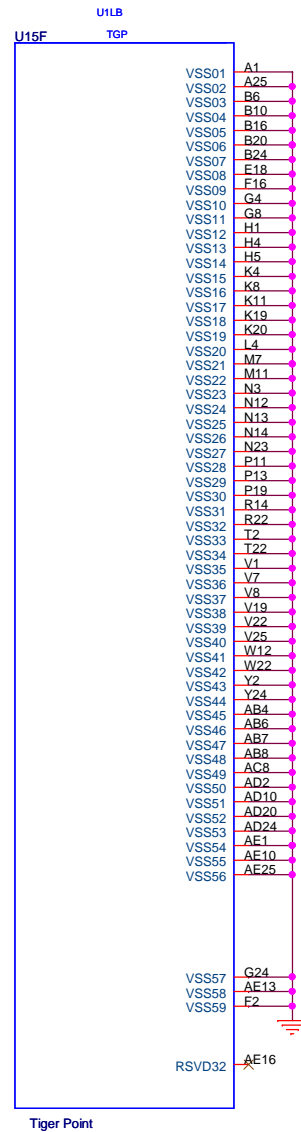
Quanta Computer Inc.


PROJECT : FL2

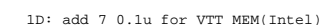
Size	Document Number	Rev 1A
TigerPoint PCI(3/6)		
Date:	Thursday, November 05, 2009	Sheet 10 of 36





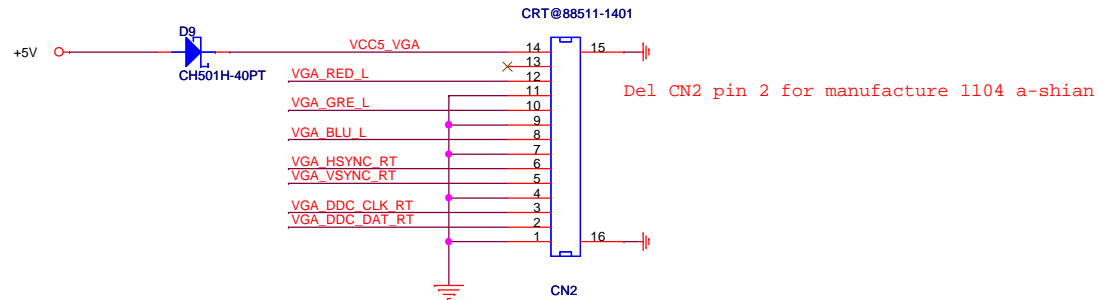
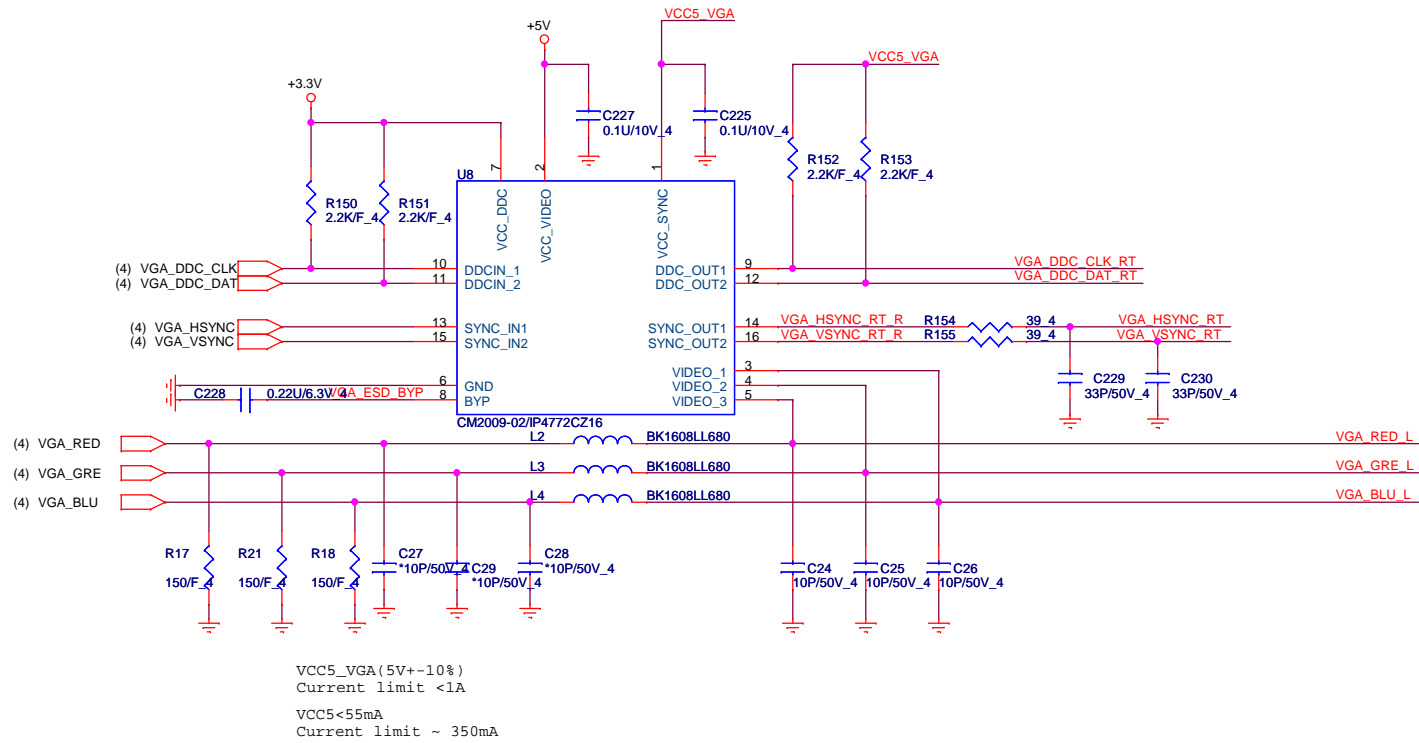



 Quanta Computer Inc. PROJECT : FL2		Rev
		1A
Size	Document Number	
TigerPoint GND		
Date:	Thursday, November 05, 2009	Sheet 13 of 36



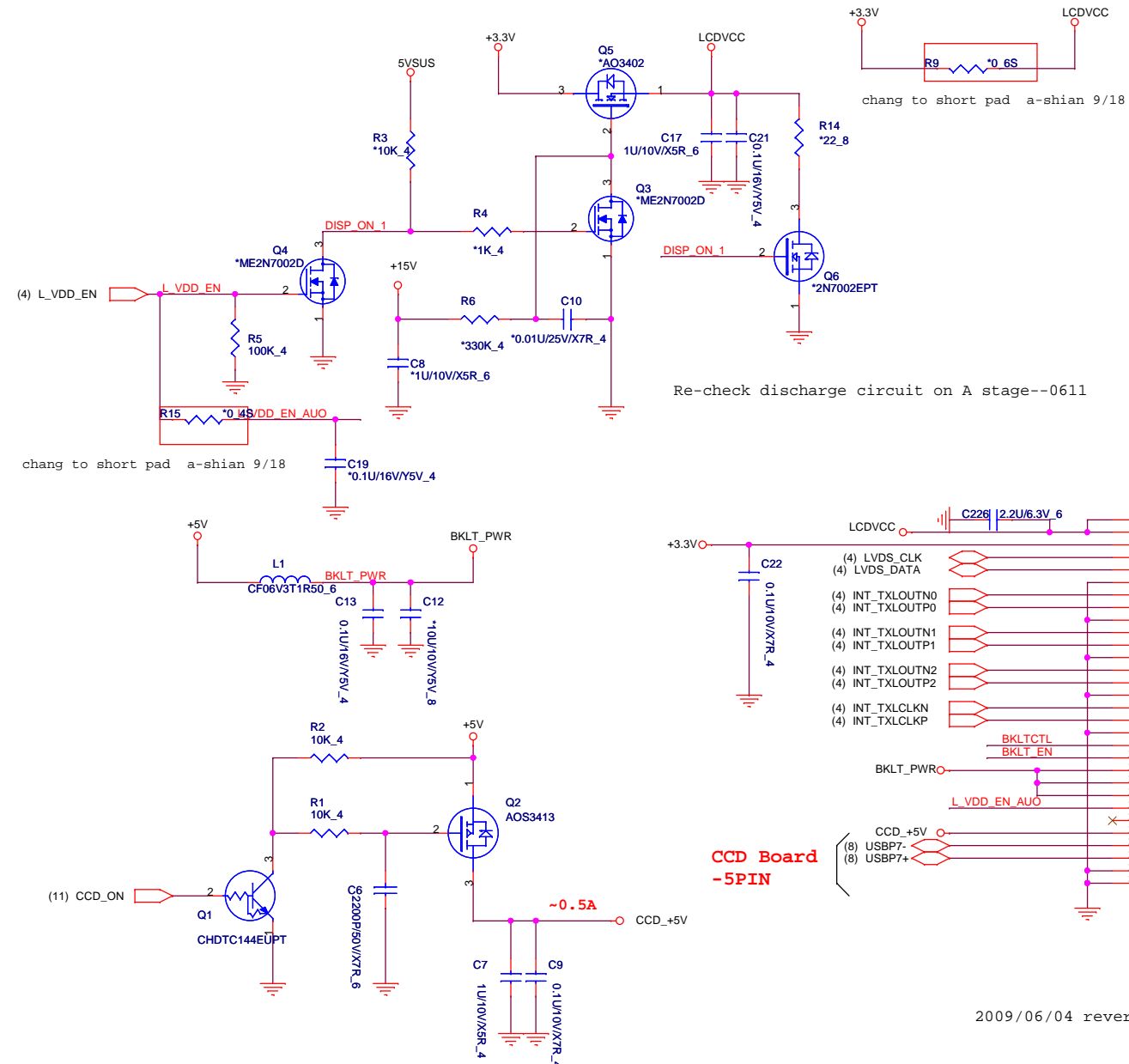
STD

(H=5.2)

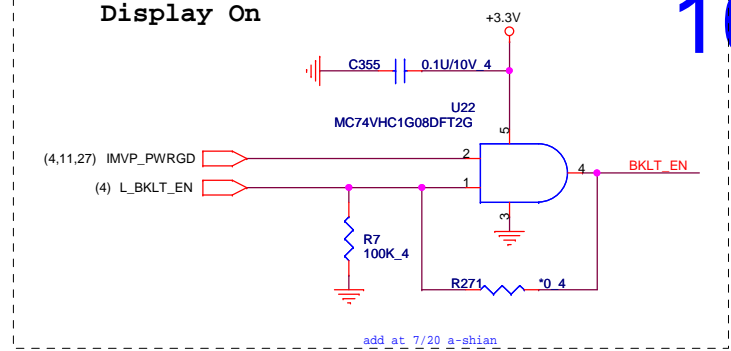


 Quanta Computer Inc. PROJECT : FL2		
Size	Document Number	Rev
	CRT	1A
Date:	Thursday, November 05, 2009	Sheet 15 of 36

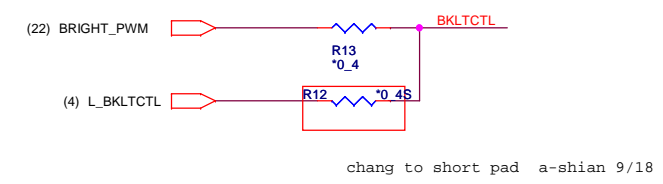
LVDS Enable



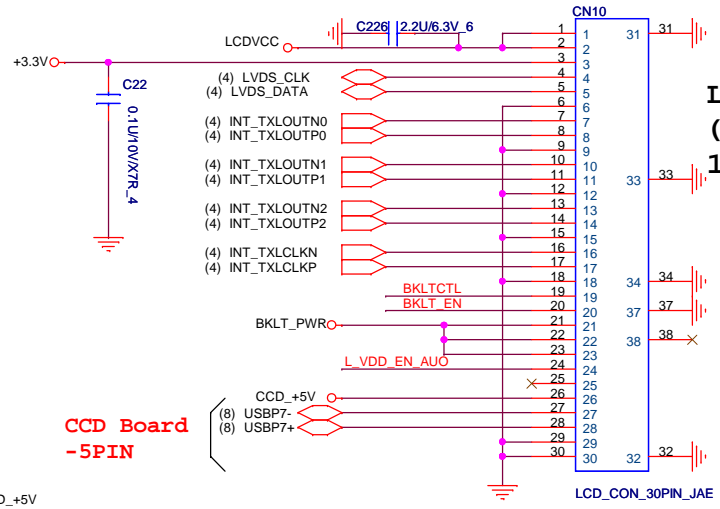
Display On



Backlight Control



**LVDS (10.1")
(1024x600,
1366x768)**



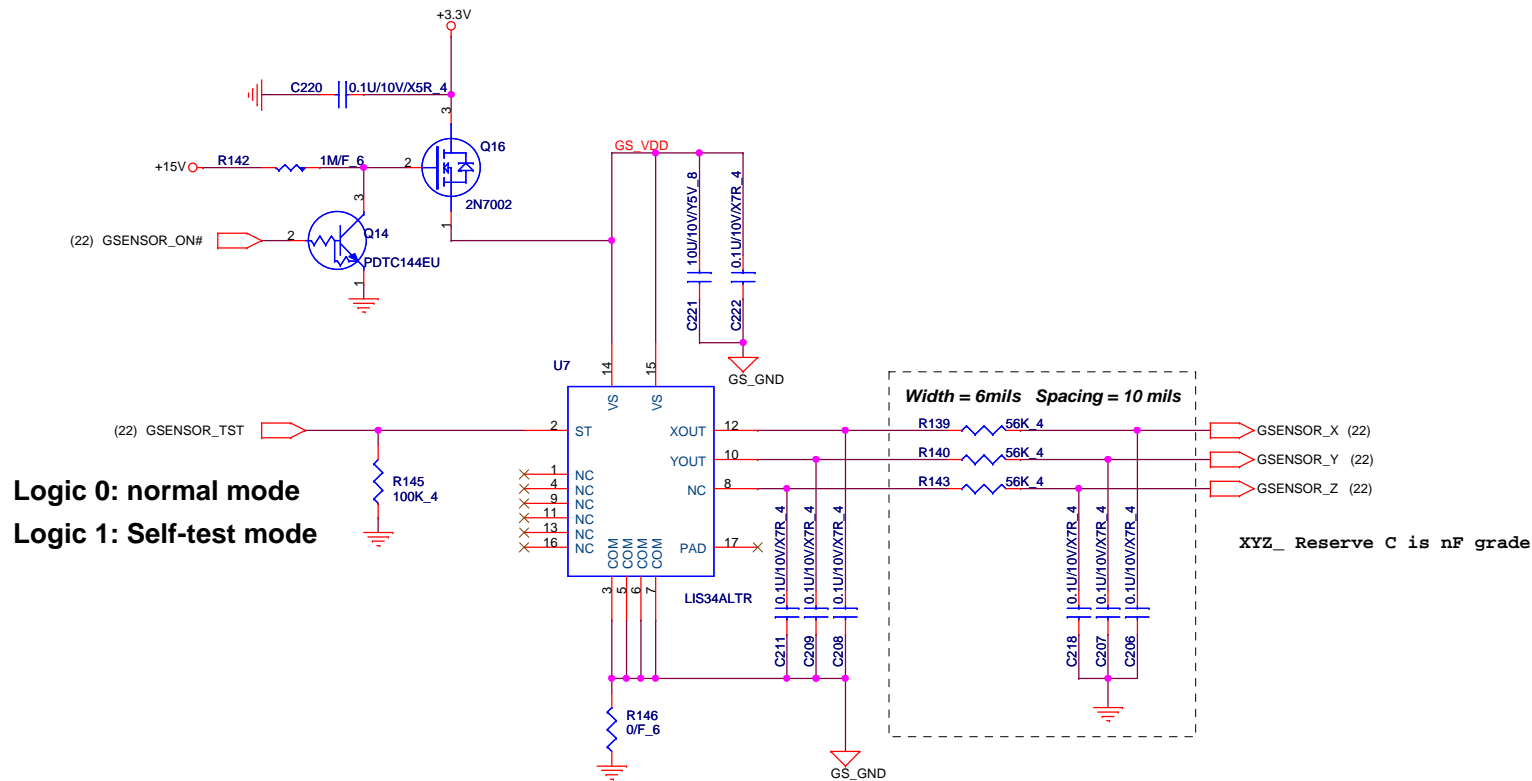
**CCD Board
-5PIN**

OK -alf
change to JAE -0601

2009/06/04 reverse for layout

Quanta Computer Inc. PROJECT : FL2		Rev
		1A
Size	Document Number	
LVDS		
Date:	Thursday, November 05, 2009	Sheet 16 of 36

G-SENSOR (3-Axial)



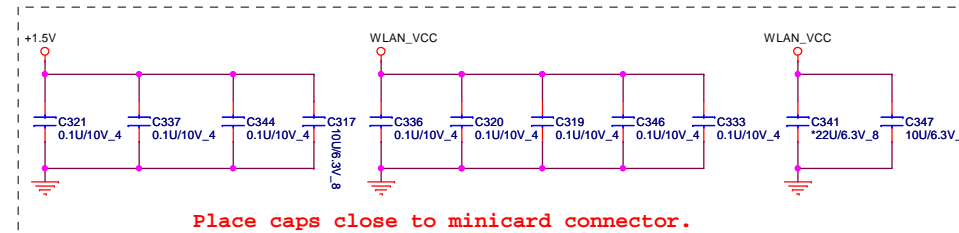
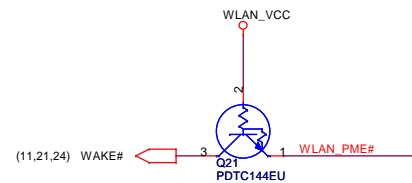
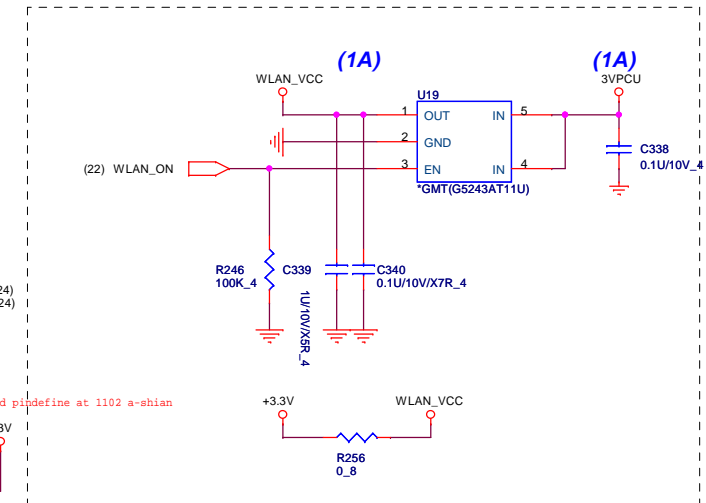
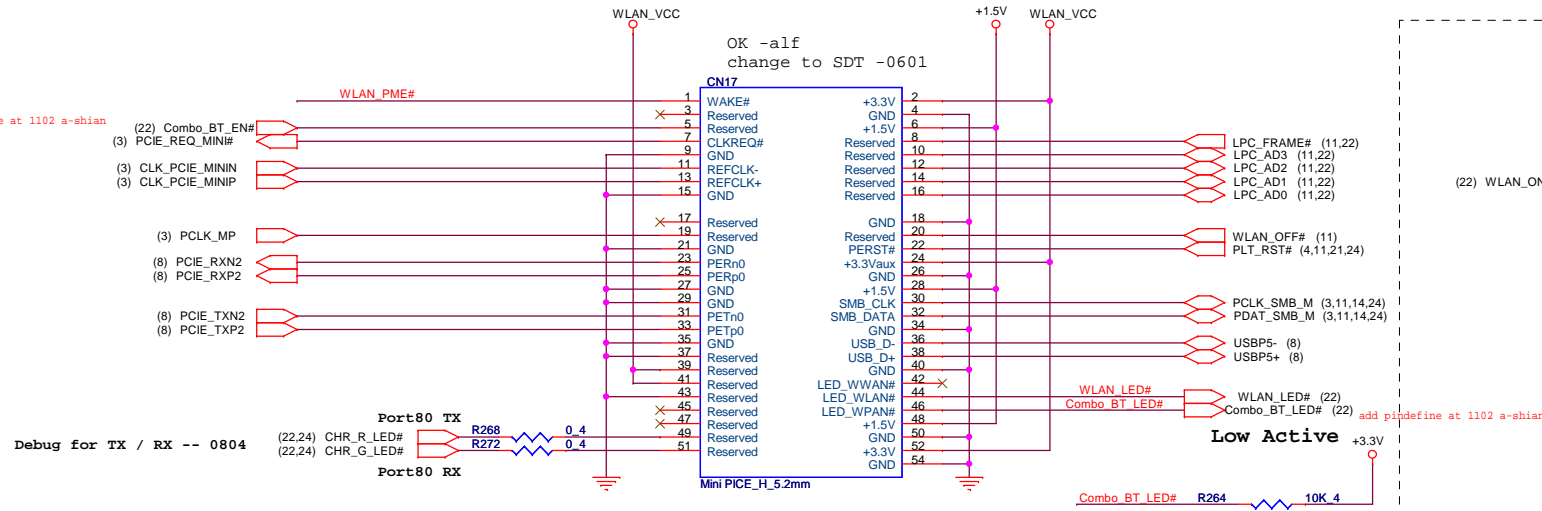
Quanta Computer Inc.

PROJECT : FL2

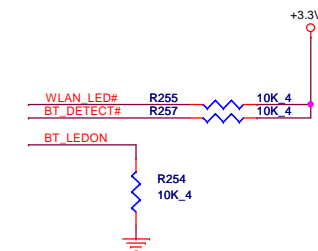
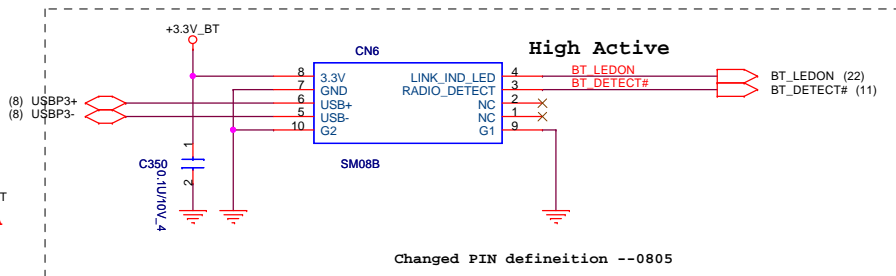
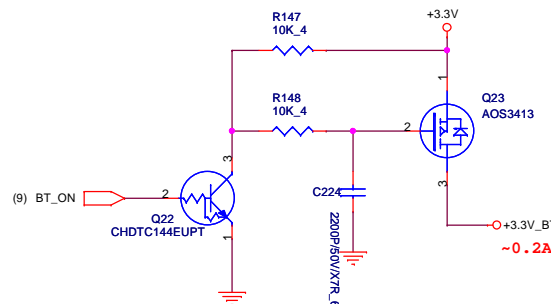
Size	Document Number	Rev
	G_Sensor	1A
Date:	Thursday, November 05, 2009	Sheet 17 of 36

3G MiniCard WLAN connector Mini Card Version1.2

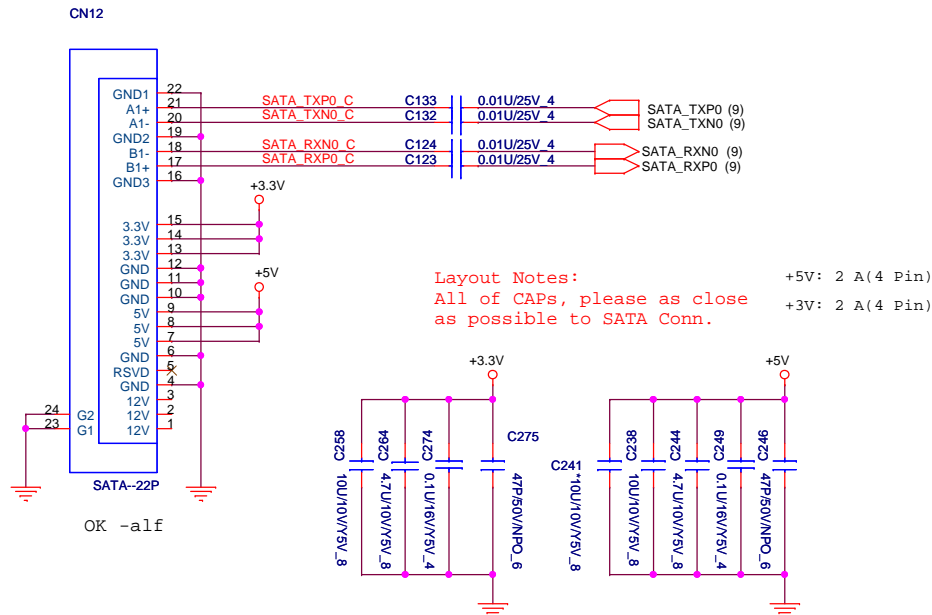
+3.3V Max 1.1A / +1.5V Max 375mA



Bluetooth

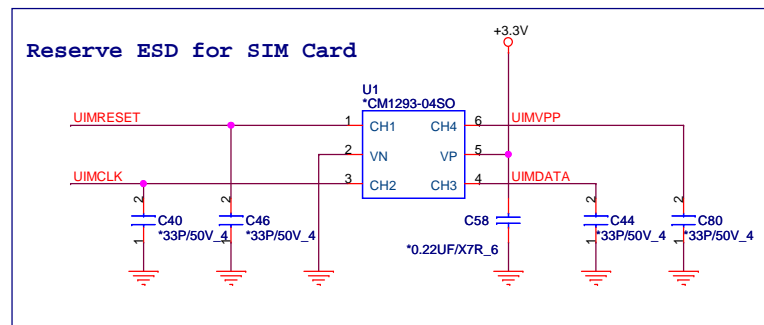
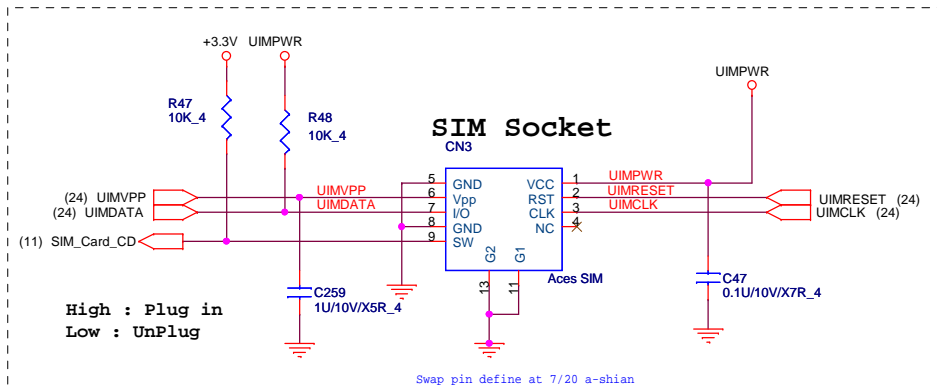


SATA CONNECTOR



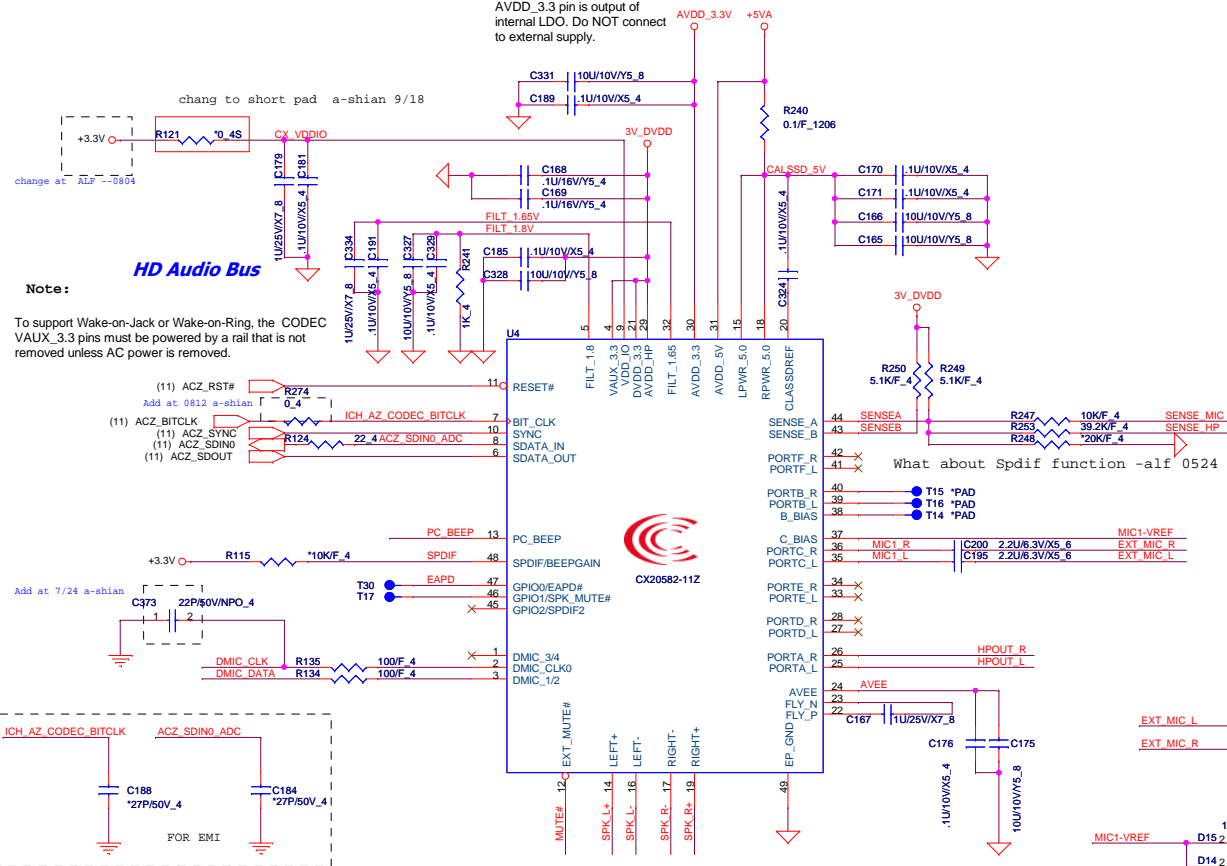
SIM Card CONNECTOR

Layout Notes for SIM Card
 SIM Power (UIMPWR) trace more than 20mil

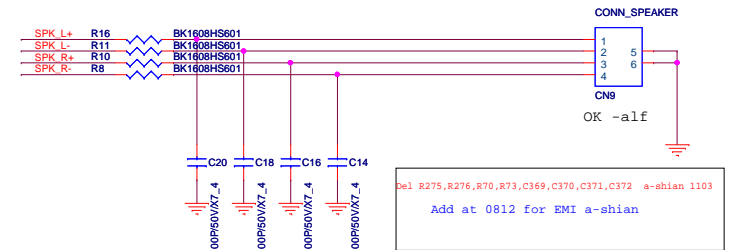
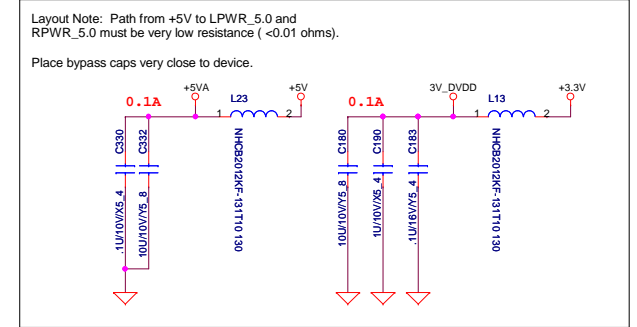
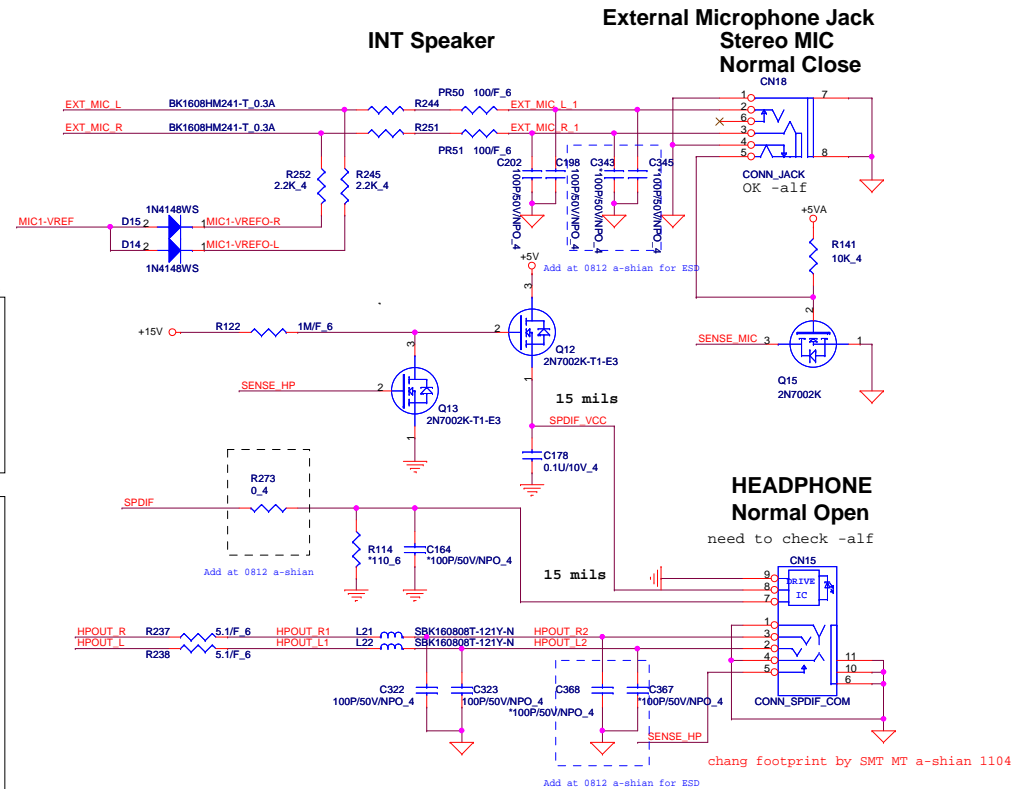
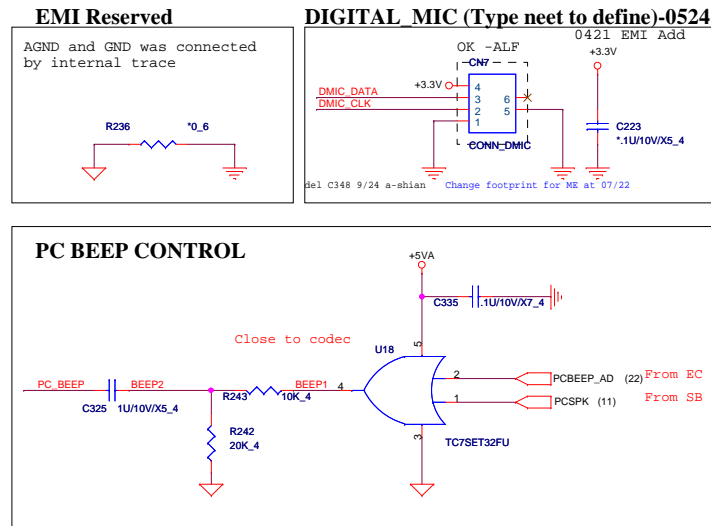
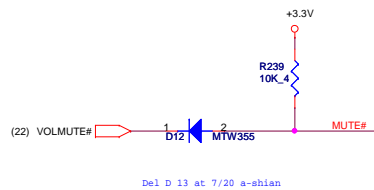


Quanta Computer Inc.
 PROJECT : FL2

Size	Document Number	Rev
	SATA / SIM Card	1A
Date:	Thursday, November 05, 2009	Sheet 19 of 36

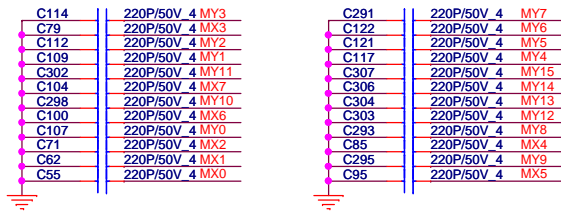
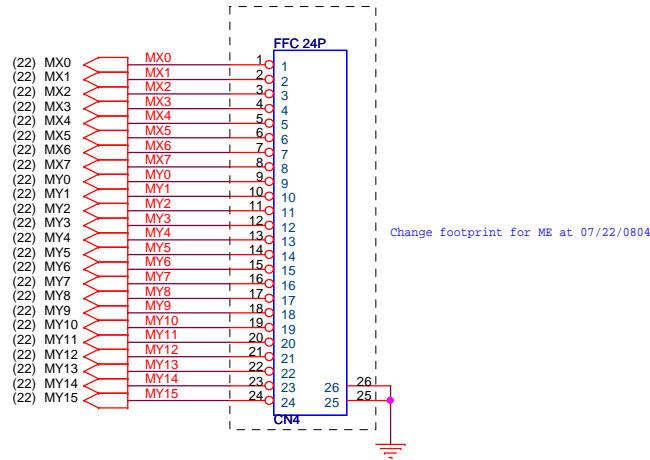


PORT	PLACE TO
MONO OUT	X
PORT A	Head Phone Out
PORT B	X
PORT C	External MIC
PORT D	X
PORT E	X
PORT F	X
DM	Internal MIC
SPDIF	V



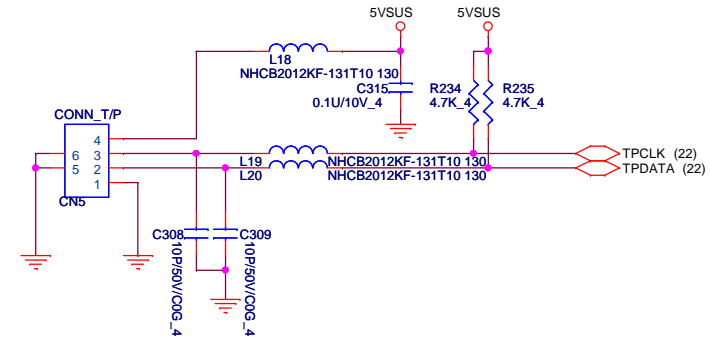


INT. KEYBOARD

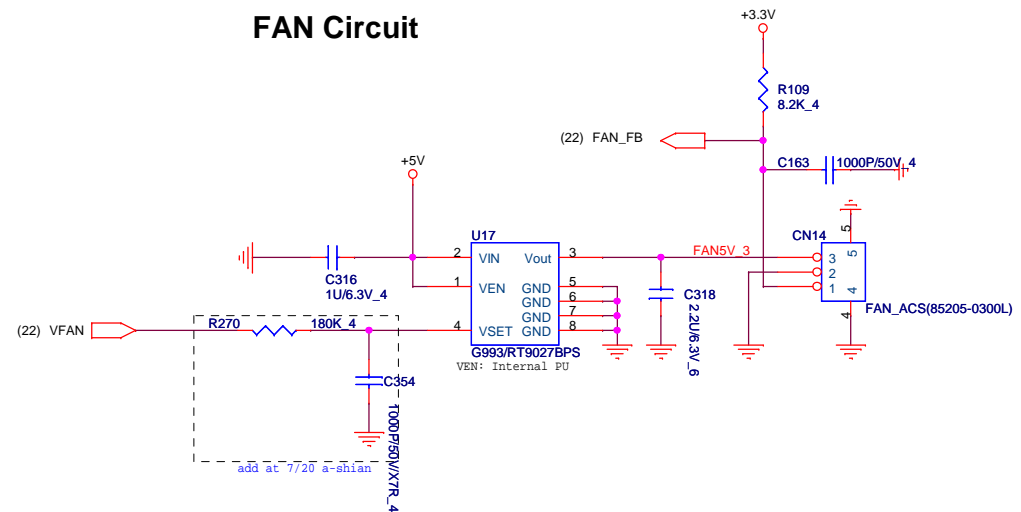


TOUCH PAD CONNECTOR

revers 6/6 for mechanism



FAN Circuit



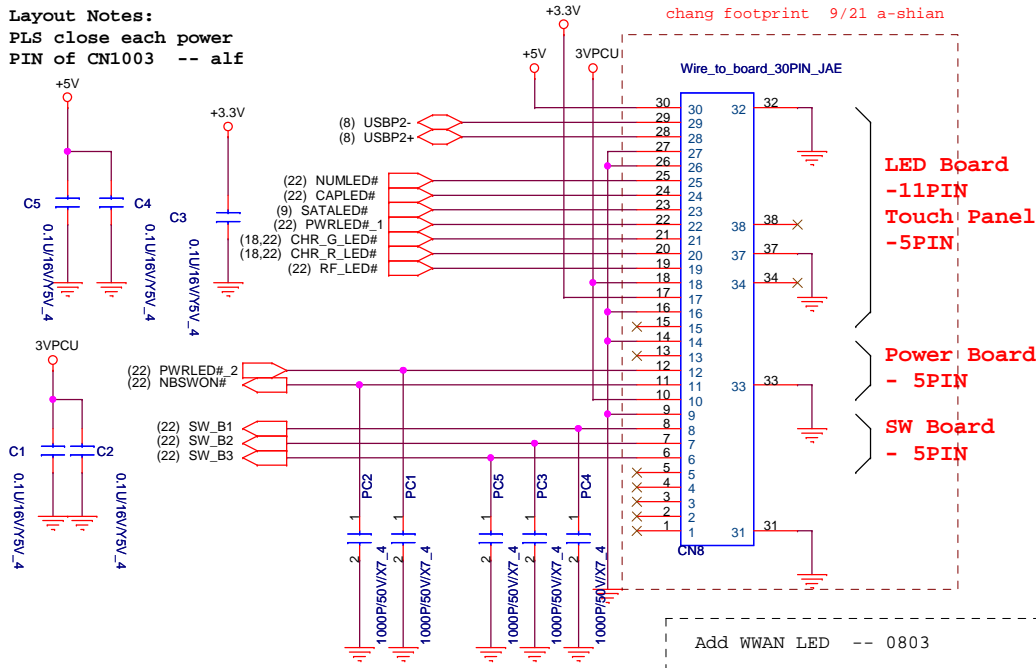
Quanta Computer Inc.

PROJECT : FL2

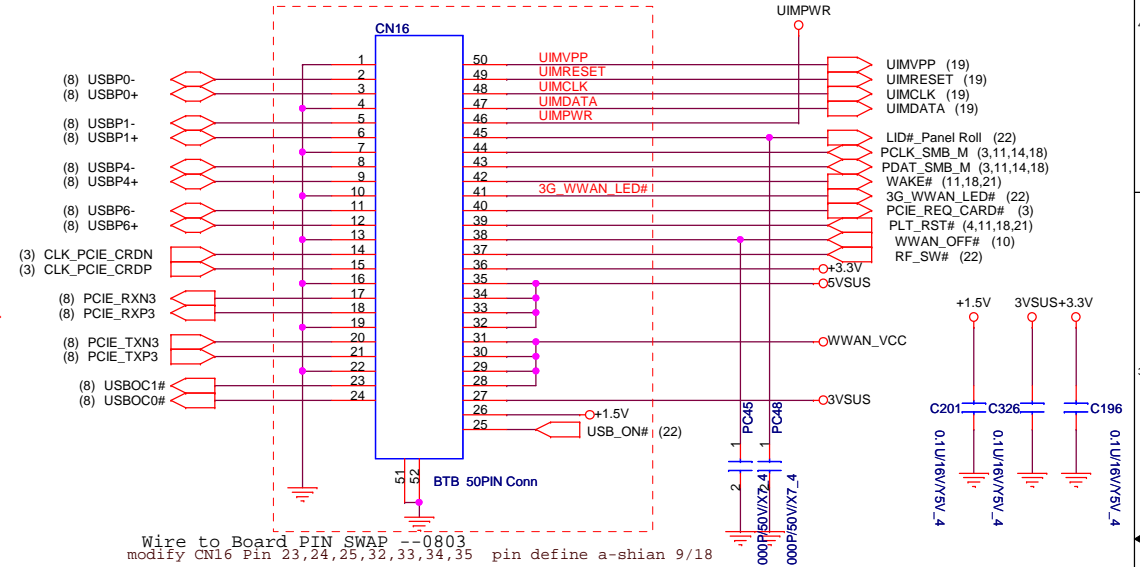
Size	Document Number	Rev
	KB / TP / FAN	1A
Date:	Thursday, November 05, 2009	Sheet 23 of 36

Layout Notes:
PLS close each power
PIN of CN1003 -- alf

LCD to MB wire

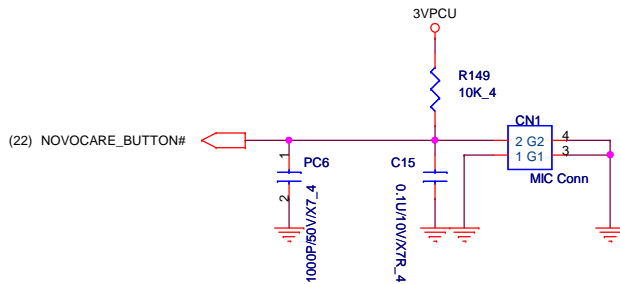


Card Reader to MB wire



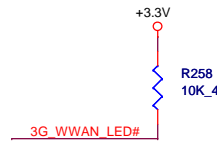
Function Board

NOVO_BUTTON#



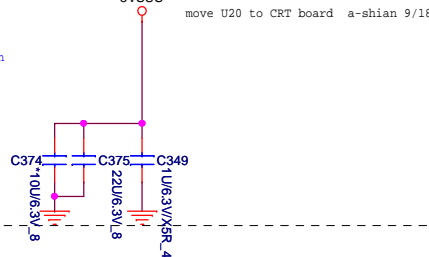
Add WWAN LED -- 0803

WWAN LED PULL UP

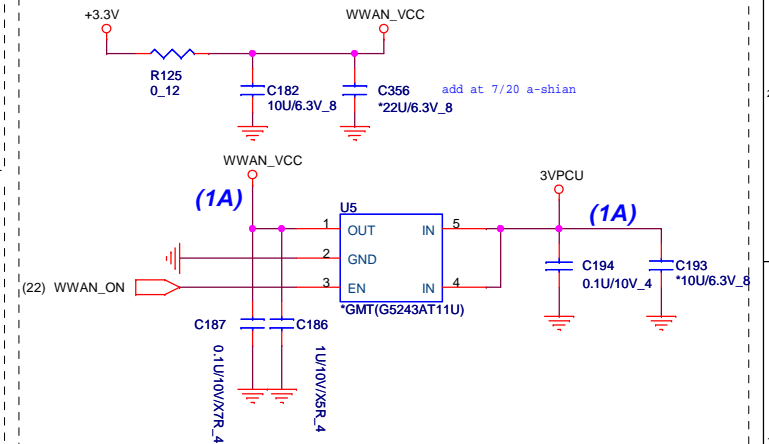


USB Power

Del R258 at 7/20 a-shian

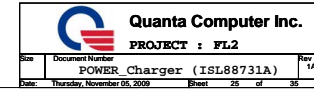


EC - Reserve Power Control for WWAN -- 0205

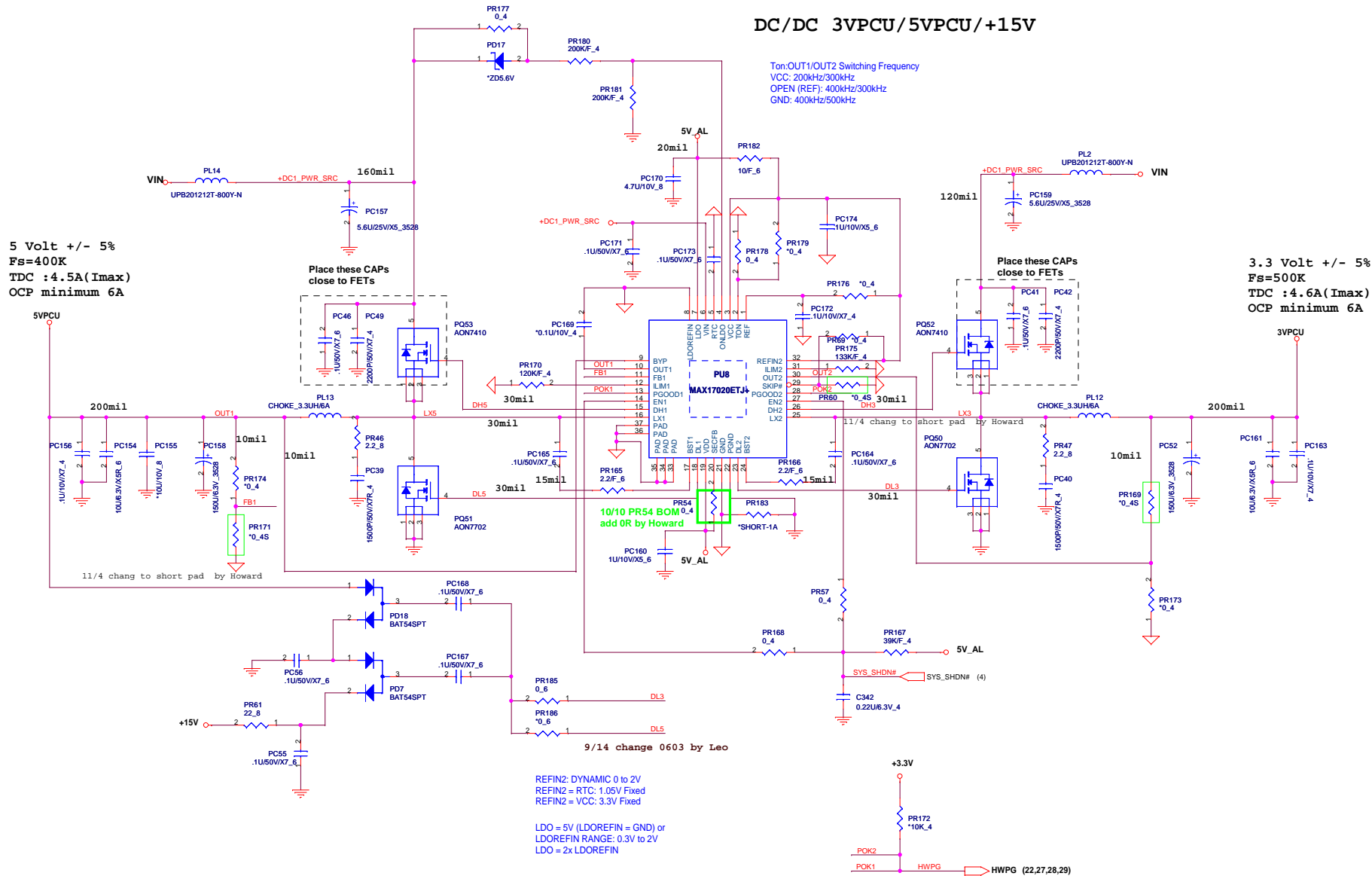


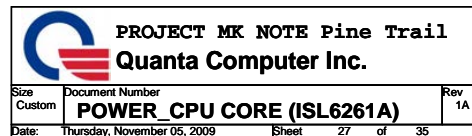
Quanta Computer Inc.
PROJECT : FL2

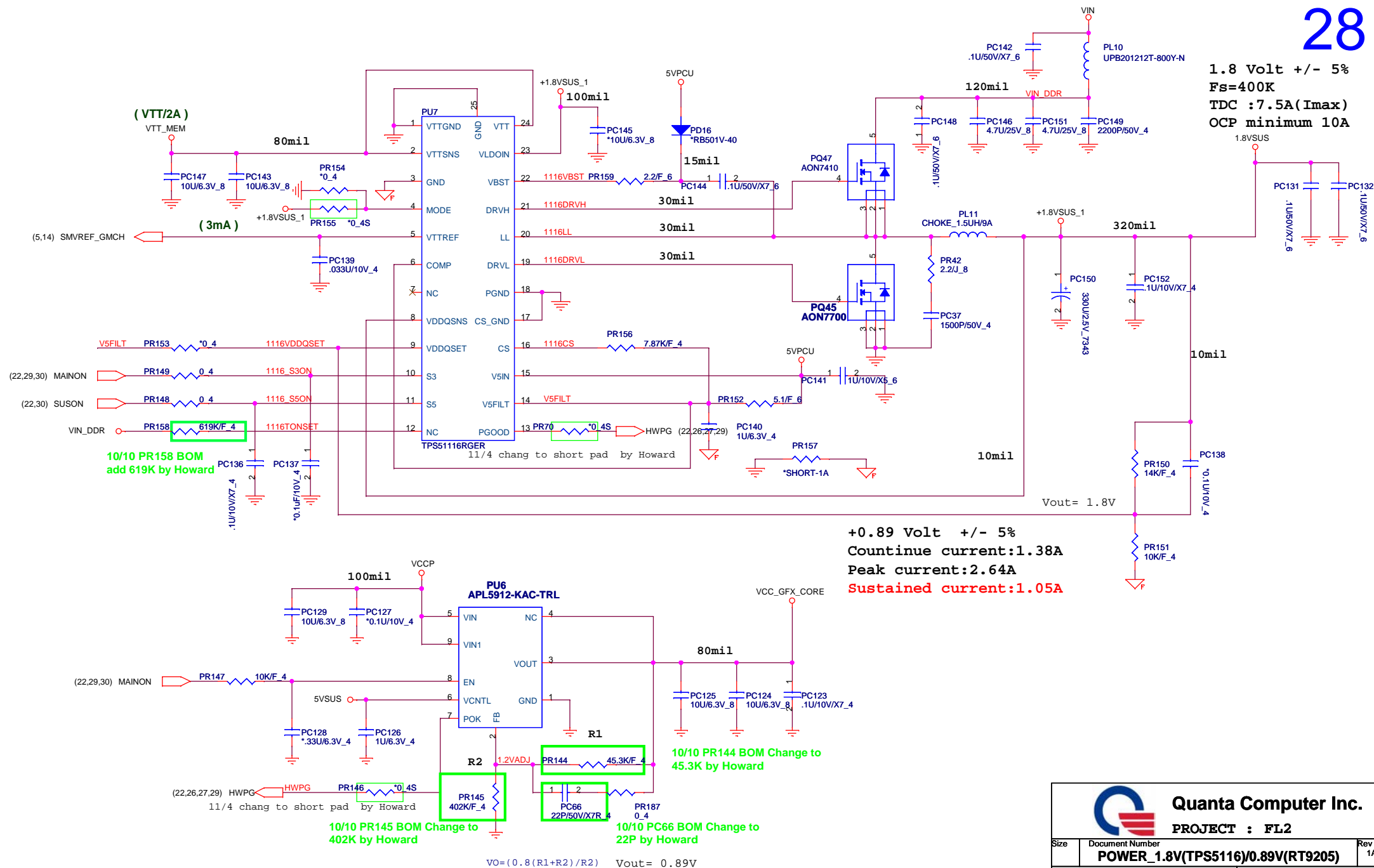
Size	Document Number	Rev
	Small Board	1A
Date:	Thursday, November 05, 2009	Sheet 24 of 36

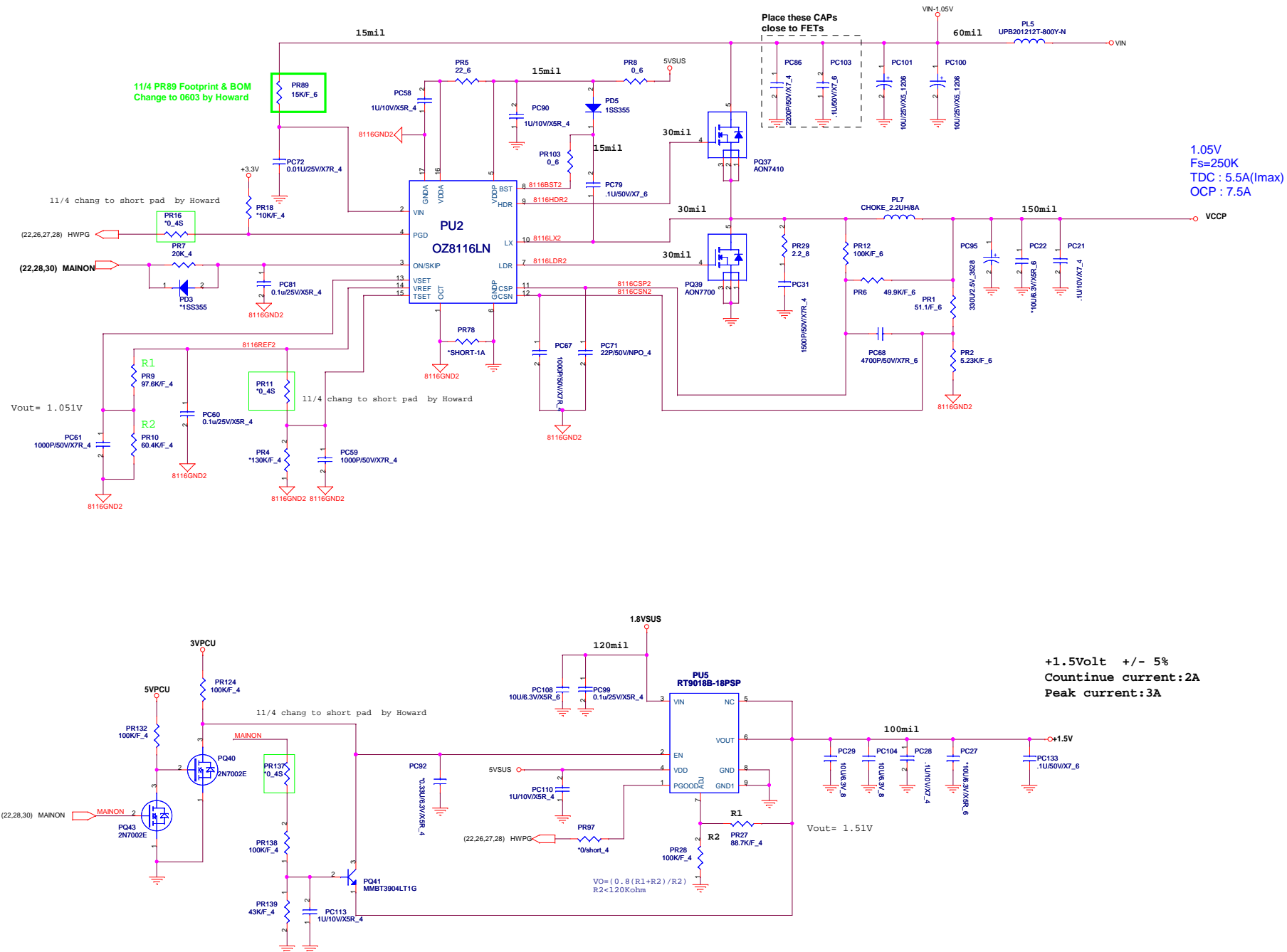


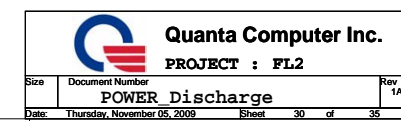
Ton:OUT1/OUT2 Switching Frequency
VCC: 200kHz/300kHz
OPEN (REF): 400kHz/300kHz
GND: 400kHz/500kHz



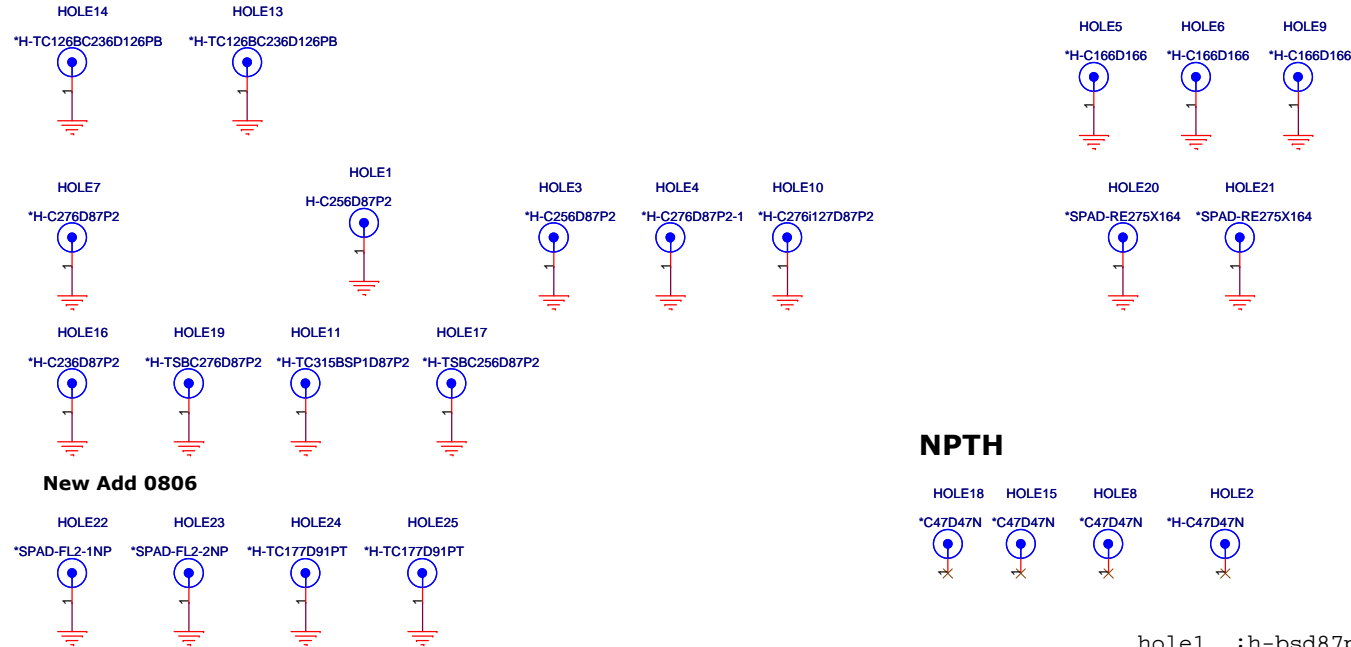








Screw Hole



hole4
hole11
hole3
hole10
hole1
hole2
hole17

NPTH

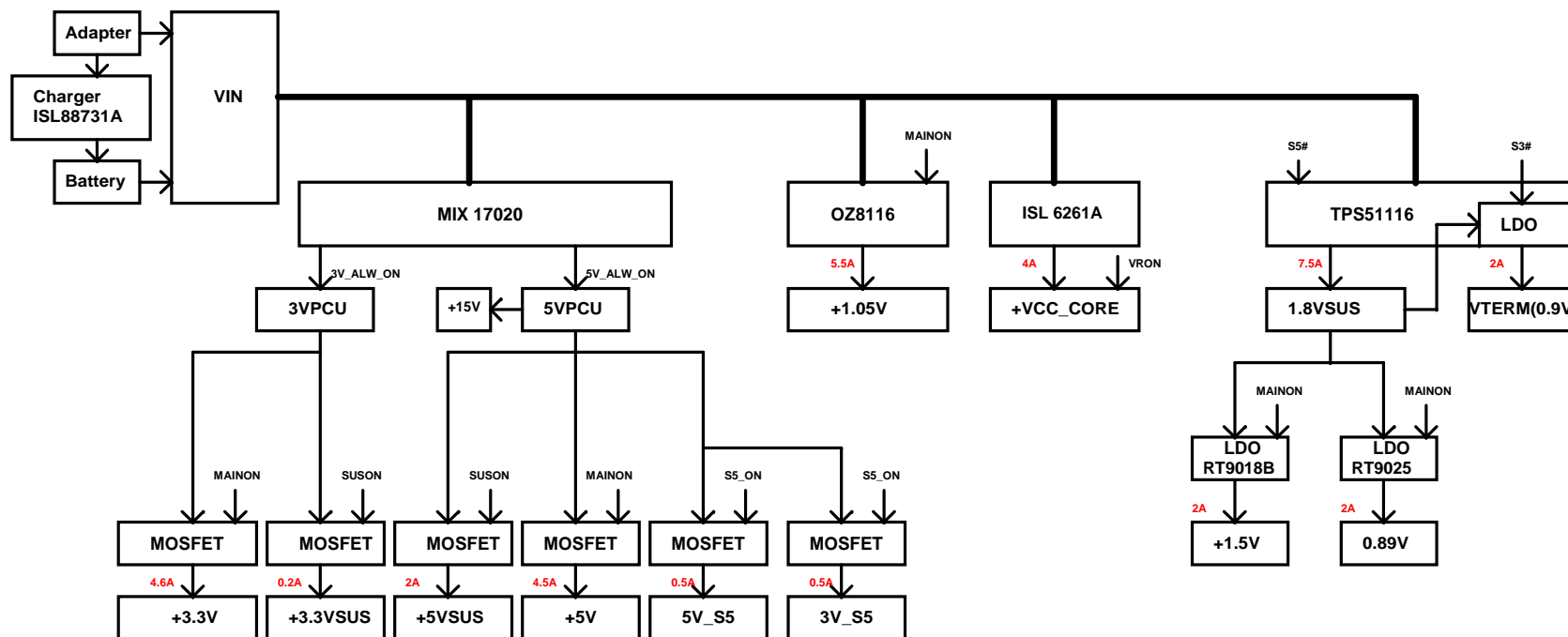
hole1 :h-bsd87pb
hole2, del
hole4, hole3, hole10, : h-c87d87n
hole16: h-c236d87p2
hole19: h-tsbcd87p2
hole11:h-bsp1d87pb
hole12 :del
hole17:h-bsd87pt

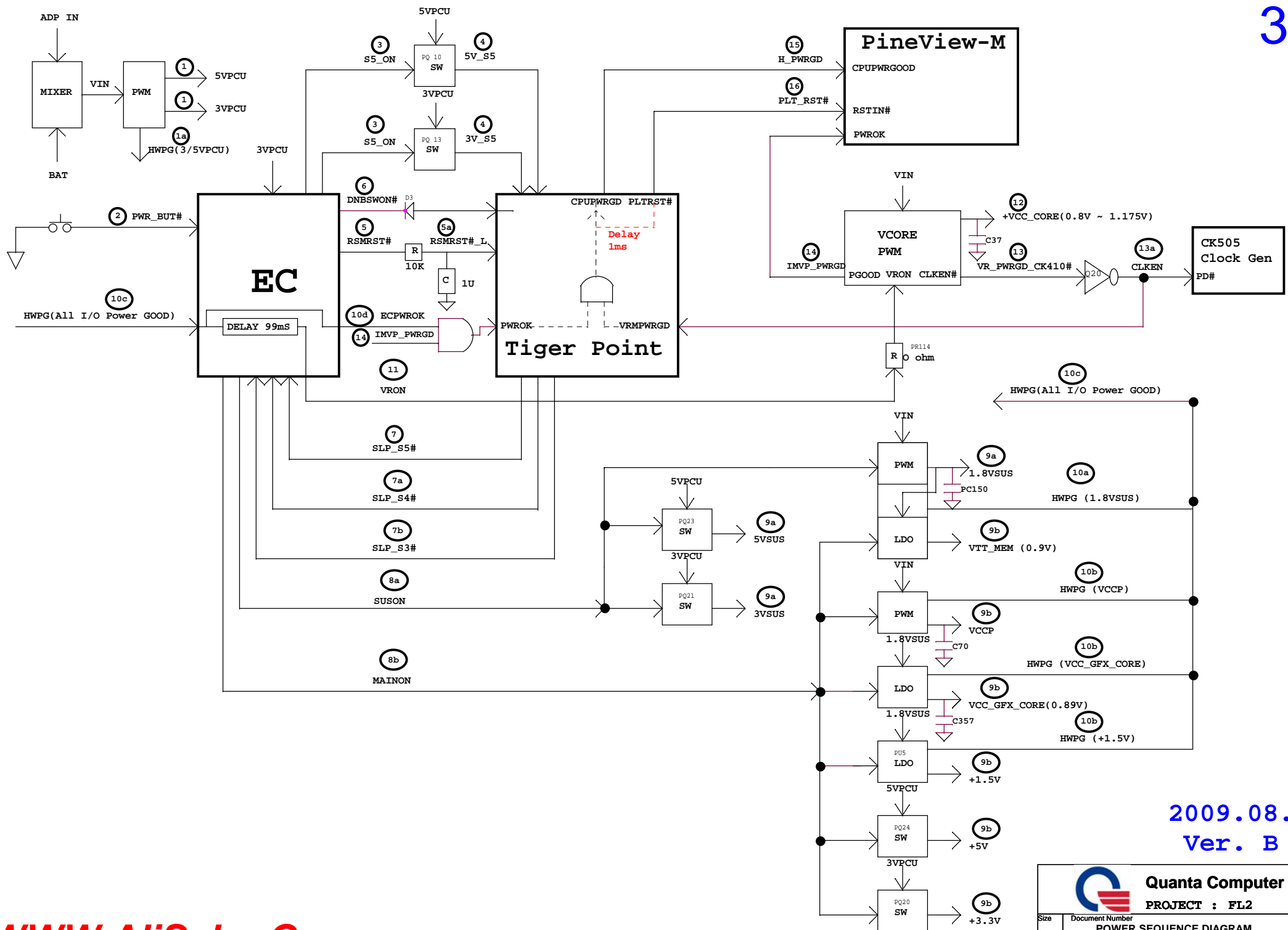


Quanta Computer Inc.

PROJECT : FL2


Size	Document Number	Rev 1A
Screw Hole / EMI		
Date: Thursday, November 05, 2009	Sheet 31 of 36	





2009.08.03
Ver. B

MODEL :		REV :	CHANGE LIST:	MODEL : FL2 MB			
FL2 MotherBoard	1A	FIRST RELEASE ON 2009/05/11		PAGE	FROM	To	
		Page 00 --> 1.		1	1A		
	2B	1. Add pull up resistor 10K on PM_SETCPU# and PM_STPPC# 2. Swap Clock GEN pin 31 to 32 3. Swap Clock GEN pin 50,51 to pin 35,34 4. Add off page symbol on CLKEN line 5. Change C297 Volume 0.01uF->0.1uF 6. Add capacitor 22uF (C352,C353) and 0.01uF(C360) on VCC_CORE 7. Swap capacitor 10uF to 22uF (C36) 8. Add capacitor 22uF(C357),0.01uF(C358) on VCC_GFX_CORE 9. Swap 0R(R73) to 600R @100MHZ(L24) Bead 10 Add 0.01uF (C361) on 1.8VSUS 11. Add 0.01uF (C362) on VCCP 12. Add 0.01uF (C363) on +1.8V 13. Swap R68 location to close chip 14 Add pull up resistor (R263) on WWAN_OFF# 15. Change R168 and R173 volume 10K-> 2.2K 16. Change sequence Add AND gate U21, C351, R261, R262 ,R264 ,R265 . 17. Add pull down resistor (R266, R267) on SA0 , SA1. 18. Improve backlight flash add U22,C355,R271 19. Modify SIM define (CN3) 20. Change audio VDD_IO voltage +1.5V->+3.3V 21. Del D13 on EAPD 22. Del D8 on SERIRQ 23. Swap EC ICH_RSMRST# (Pin33 -> Pin 99) 24. Add pull up resistor 10K (R269) on LID#_Panel Roll 25. Add 180K (R270) and 1000pF (C354) on FAN Circuit 26. Del R258 on USB_ON# 27. Add 22uF (C356) on WWAN_VCC 28. Del PR69 on HWPB 29. Del PR89A and PD10 on VRON Lline 30. Add R273 on SPDIF line 31. Add R274 on ICH_AZ_CODEC_BITCLK 32. Add C102 (10p) in BOM 08/12 33. L10 10uH ->0R 08/12 34. R150,R151 3.9K ->2.2K08/12 35. R14,R3,Q4,Q6, del in BOM (reserve)08/12 36. Swap CN7 part number (DMIC)08/12 37. Reserve R70,R275,R276,R73,369,C370,C372,C371 and C343,C345,C368,C367 in audio function for EMI and ESD08/12 38. Reserve R274, PC177, R273 in audio function for EMI 08/13 39. Cancel CHS_GND in LAN function of RJ4508/13 40. Swap S1 part number (Hall sensor)08/13 41. Swap CN4 part number (Key board connector)08/13 42. Add C55,C62,C71,C79,C85,C95,C100,C104,C107,C109,C112,C114,C117,C121,C122,C291,C293,C295,C298,C302,C303,C304,C306,C307 for EMI in Keyboard connector08/13		2	1A		
	3c	1.Del R101,R196 OHM 09/22'09 2.R189,R90,R201,R159,R74,R191,R205,R83,R177,R265,R121,L17,R9,R125,R12,R256 Use short pad. 09/22'09 3.change CN9 footprint 09/22'09 4.modify CN16 pin define 09/22'09 5. move U20 to CRT board		3	1A		
	4D	1.DEL 0R R275,R276,R70,R73 FOR audio output and DEL R221 for MB ID 2.ADD R222 for MB ID and ADD R264 10K 3.Change audio phone jack footprint CN13 4.DEL CN2 pin 2 Let it floating.		4	1A		
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Quanta Computer Inc.

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Size

Document Number

Rev 1A

FL2 EC Record List_A

Date: Thursday, November 05, 2009

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